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Skillfully as picked to act.

Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is

Q. 5.8: Derive the state table and the state diagram

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of the sequential circuit
shown in Fig. P5.8 ~~Digital~~
~~Design: Q: 1.6:~~ The
~~solutions to the quadratic~~
~~equation $x^2 - 11x + 22 = 0$ are~~
 ~~$x = 3$ and $x = 6$.~~ Q. 7.19:
Tabulate the PLA programming
table for the four Boolean

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~~Solutions File Type~~ ~~Q.~~

~~3.9: Find all the prime
implicants for the following
Boolean functions, and
determine which are~~ Q. 6.10:
Design a serial 2's
complementer with a shift
register and a flip?flop.

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*Finite State Machines -
State Table, State Diagram
and Sequence of Inputs
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*Introduction Process --
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state diagram/state
table/circuit diagram (using
D-flip flop) - Digital Logic
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VHDL Example 10: Generic
MUX - Parameters.ppt

*Sequential Circuit Analysis
- From sequential circuit to
state transition diagrams.*

~~Lecture 13 (EECS2021E) -~~

~~Appendix A - Digital Logic -~~

~~Part I Lesson 11 - VHDL~~

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Example 3: Majority Circuit

Lesson 15 – FPGAs Q. 5.10: A sequential circuit has two JK flip-flops A and B, two inputs x and y, and one output z Q. 4.1: Consider the combinational circuit shown in Fig. P4.1.(a) *

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*Derive the Boolean
expressions fo*

Digital Design: Q. 1.13: Do
the following conversion
problems: (a) Convert
decimal 27.315 to binary.

1.1: List the octal and
hexadecimal numbers from 16

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~~to 32. Using A and B for the
last two digits Q. 3.15:~~

~~Simplify the following
Boolean function F, together
with the don't care
conditions d, and Q. 1.33:~~

**The state of a 12-bit
register is 100010010111.**

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What is its content if it

represents Q. 5.4: A PN flip-

flop has four operations:

clear to 0, no change,

complement, and set to 1,

when sinc function |

Properties | unnormalized

sinc function | Normalized

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sinc function | Graph

explained Q. 4.8: Design a code converter that converts a decimal digit from the 8, 4, -2, -1 code to BCD

Q. 5.1: The D latch of Fig. 5.6 is constructed with four NAND gates and an inverter.

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Q. 2.4: Reduce following
Boolean expressions to the
indicated number of literals

(a) $A'C' + ABC + AC'$ *Book*

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computer Design by Morris
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*book Review Q. 5.7: A
sequential circuit has one
flip-flop Q , two inputs x
and y , and one output S . It
consists*

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a deck with 52 cards, we need 6 bits ($32 < 52 < 64$). Let the msb's select the suit (e.g., diamonds, hearts, clubs, spades are encoded respectively as 00, 01, 10, and 11. The remaining four bits select

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the "number" of the card.

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