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FPGA IMPLEMENTATION - Step By Step - Digital System Design

FPGA synthesis is performed by dedicated synthesis tools. Cadence, Synopsys and Mentor Graphics are EDA companies that develop, sell and market FPGA synthesis tools. Implementation This phase is where the layout of your design will be determined and consists of three steps: translate, map, and place & route.

The Ultimate Guide to FPGA Design Flow - HardwareBee

Implementing a solution on FPGA includes building the design using one of the design entry methods such as schematics or HDL code such as Verilog or VHDL, Synthesizing the design (Synthesis, netlist generation, place, and route, etc.) into output files that FPGAs can understand and program the output file to the physical FPGA device using programming tools.

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