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Questa combines all
of these forms of

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Stimulus generation with functional coverage to identify the functionality exercised by the automatically generated stimulus. Using functional coverage metrics (SVA or PSL) as feedback for test creation, engineers can adjust constraints to focus

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coverage holes.

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Computer

Engineering

This document becomes the guide for the verification process. Within Questa ' s user interface the plan ' s data can be sorted, filtered and subjected to complex queries such as which tests

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Guido are most effective at testing this particular feature or which set of tests needs to be run to get the best coverage for a modified instance of the design.

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Manual - College of
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Computing

Tool Introduction
QuestaSim is part
of the Questa
Advanced
Functional
Verification
Platform and is the
latest tool in Mentor
Graphics tool suite
for Functional
Verification. The
tool provides
simulation support

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for latest standards
of SystemC,
SystemVerilog,
Verilog 2001
standard and VHDL.

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Questa combines all
of these forms of
stimulus generation
with functional

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Coverage to identify the functionality exercised by the automatically generated stimulus. Using functional coverage metrics (SVA or PSL) as feedback for test creation, engineers can adjust constraints to focus random testing on coverage holes.

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Questa Advanced
Simulator - Mentor
Graphics

Questa Advanced
Simulator - Basic
On-Demand

Training Course.

Learn to use
Advanced Questa
Simulator GUI and
command line to
verify and debug
HDL designs in

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Interactive mode or
build batch mode
scripts for fast
simulations....

Questa & ModelSim
- Mentor Graphics
Questa CoverCheck
automatically
traverses your
DUT's state spaces
and identifies
unreachable areas,
enables the user to

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Guide
"waive"/exclude items from future analyses, and pipe all results into a Unified Coverage DataBase (UCDB) for inclusion in Questa Verification Management analyses and progress reporting.
The Solution:
Questa CoverCheck

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Questa CoverCheck
- Mentor Graphics
Using only your
RTL (and UPF
power intent file),
Questa CDC
solutions
automatically
generate and
analyze assertions
to rapidly identify
chip-killing clock-
domain crossing
(CDC) issues.

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Results can also be transmitted to the master verification progress database via UCDB.

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ModelSim User's
Manual
ModelSim
Reference Manual,
v6.5e 11 Chapter 1
Syntax and

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Conventions

Documentation

Conventions This manual uses the following

conventions to define ModelSim™ command syntax.

Note Neither the prompt at the beginning of a line nor the <Enter> key that ends a line is shown in the

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Guide Command examples.

Table 1-1.

Conventions for
Command Syntax

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Information

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Italian, Spanish and

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Conversation Virgil

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Southwest USA

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Database

Applications Users

Guide and

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An Interpretive

Guide to Operatic

Arias The Blue And

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Forest (N.F.),
Elsmere Solid
Waste Management
Facility, Angeles
National Forest
(N.F.) Land
Adjustment Plan,
Angeles County
Iconologia della
gloriosa Vergine
Madre di Dio, Maria,
Protettrice de

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ragiona delle
immagini di Nostra
Signora, che si
riveriscono
ne' Tempij ... della
città di Messina;
delle loro origini ...
e singolari
avvenimenti. Con
alcune Digressioni
delle Persone
segnalate ...
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