

Solution Of Digital Logic And Computer Design By Mmorris Mano

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Q. 4.1: Consider the combinational circuit shown in Fig. P4.1.(a)* Derive the Boolean expressions for

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Digital circuit is a connection of two or more logic gates Digital network can be described using schematic diagrams, Boolean expressions, or truth tables x y z

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a) $(BEE)_r = (2699)_{10} = 11 \times r^2 + 14 \times r + 14 \times r^0 = 2699 = 11 \times r^2 + 14 \times r + 14 = 0$. By the quadratic equation: $r = 15$ or $r = -16.27$ ANSWER: $r = 15$ b) $(365)_r = (194)_{10} = 3 \times r^2 + 6 \dots$

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Solution: Inputs: 8-bits (A = 4-bits , B = 4-bits) A and B are two 4-bit numbers 4-bit input 4-bit magnitude GT Let $A = A_3A_2A_1A_0$, and A comparator Let $B = B_3B_2B_1B_0$ EQ 4-bit input Inputs have 28 (256) possible B combinations (size of truth table LE

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Digital logic circuits. Numberin whatever base Decimal value of the given number. Decimal: $1,998 = 1 \times 10^3 + 9 \times 10^2 + 9 \times 10^1 + 8 \times 10^0 = 1,000 + 900 + 90 + 8 = 1,998$. Binary: $11111001110 = 1 \times 2^{10} + 1 \times 2^9 + 1 \times 2^8 + 1 \times 2^7 + 1 \times 2^6 + 1 \times 2^3 + 1 \times 2^2 + 1 \times 2^1 = 1,024 + 512 + 258 + 128 + 64 + 8 + 4 + 2 =$

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1,998.

~~DIGITAL LOGIC CIRCUITS—Engineering~~

Digital logic becomes a Sudoku. Its a matter of "0" & "1" and in the beginning of the concept, lots of 0's are 1 and vice versa. Logic gate "OR" & "AND" mismatched (interchanged). Totally confused. Took the help of other books. Read more. 8 people found this helpful. Report abuse.

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